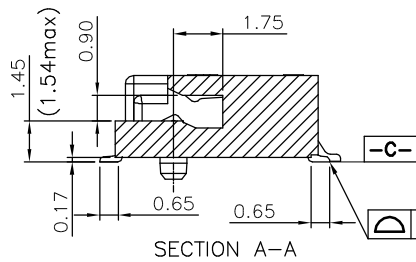
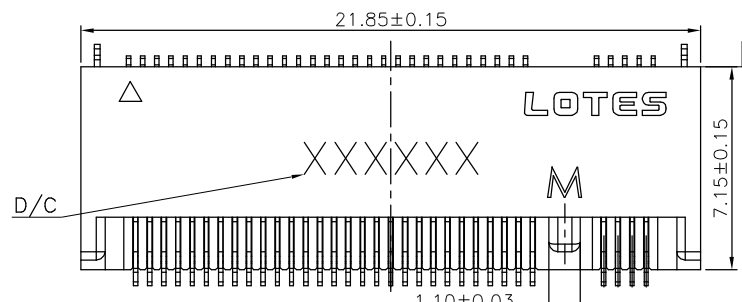


REV.	ECR/N NO./DESCRIPTION	DATE	DRAWN	CHECKED	APPROVE
A	SN19***	10/24'19	Terry	Vito	Frank
A1	SN22***	02/16'21	Reber	Vito	Herch

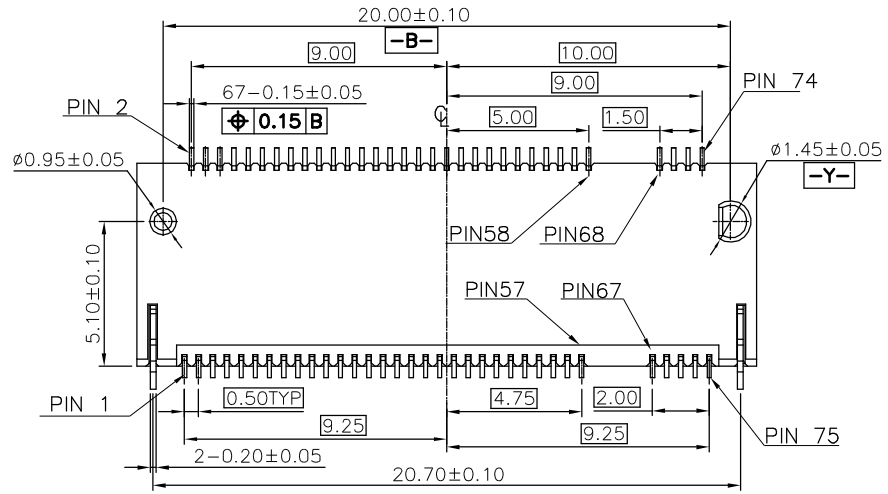
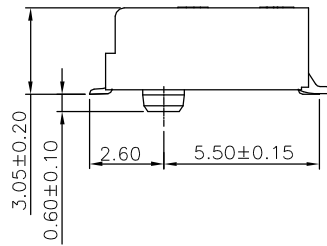
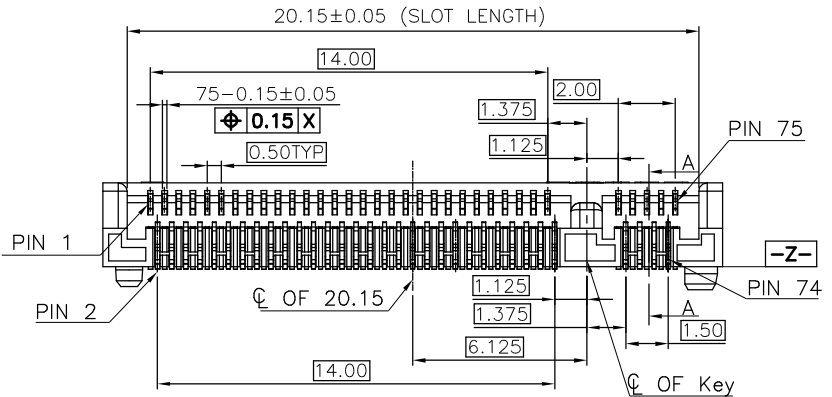


NOTES:

- MATERIAL SPECIFICATION:
  - HOUSING: LCP+40%GF, UL94V-0, COLOR: BLACK.
  - CONTACT: COPPER ALLOY.
  - SMT TAB: S50C.
- PLATING SPECIFICATION:
  - CONTACT:
    - 50u" MIN. NICKEL UNDER PLATING OVER ALL. G/F PLATING ON SOLDER AREA. GOLD PLATING ON CONTACT AREA: SEE TABLE
  - SMT TAB:
    - 50u" MIN. NICKEL UNDER PLATING OVER ALL. 80u" MIN. MATTE TIN PLATING ON SOLDER AREA.
- HF COMPLIANT, ROHS COMPLIANT.
- DATE CODE: XXXXXX
  - DAY
  - WEEK
  - YEAR
- MECHANICAL PERFORMANCE:
  - DURABILITY: SEE TABLE
- ELECTRICAL PERFORMANCE:
  - CURRENT: 0.5A PER PIN.
  - LLCR: INITIAL 55mΩ MAX. ; FINAL ΔLLCR=20mΩ MAX..
- IR REFLOW :
 

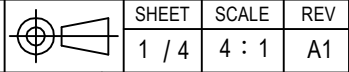
THE TEMPERATURE SHALL BE 260±5°C MAINTAINING 10±1 SECONDS.
- PRODUCT NUMBER NOTE
 

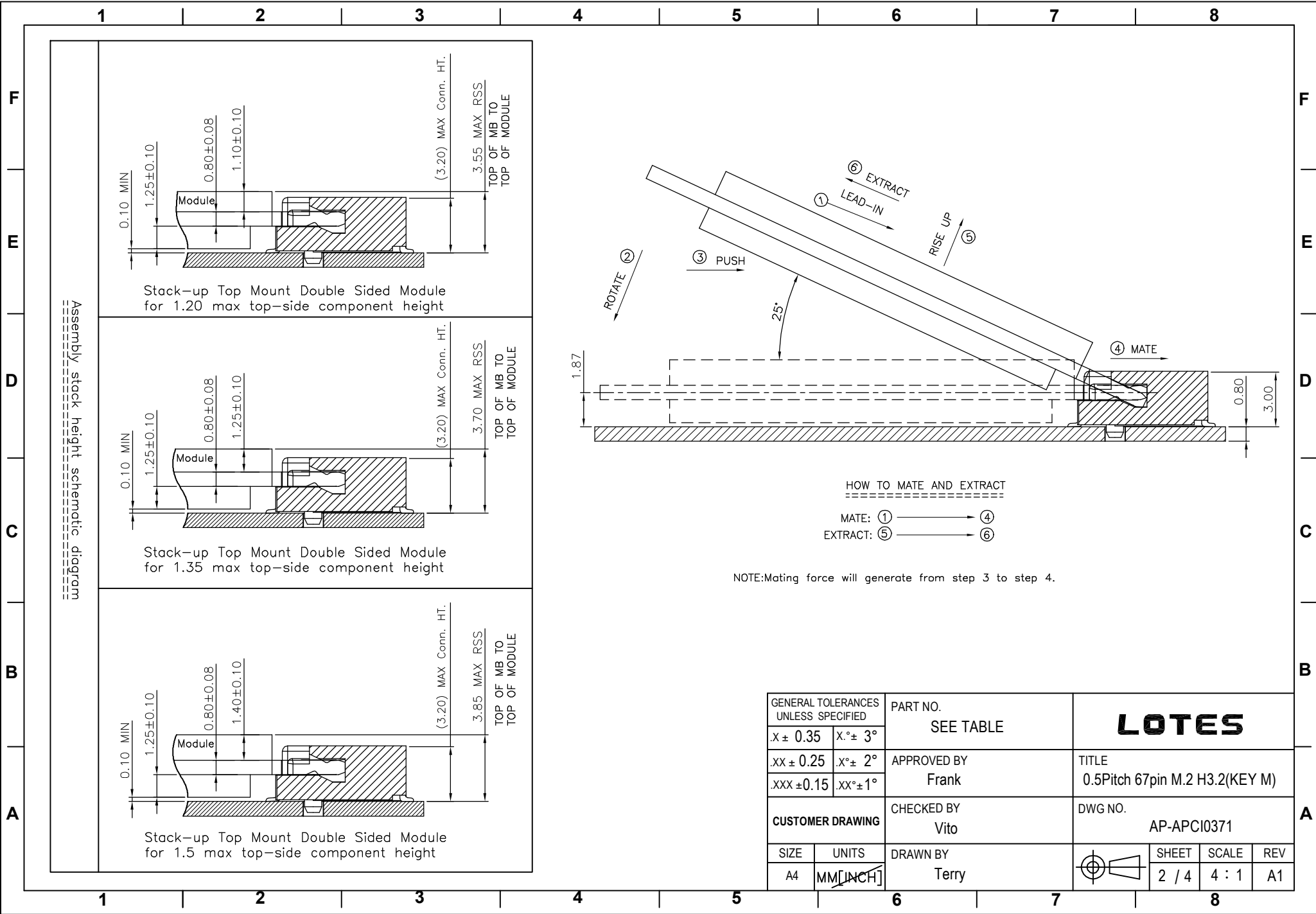
A	P	C	I	X	X	X	X	X	X	X	X	X	X
				PACKING TYPE		PRODUCT SERIAL NUMBER		K:ROHS COMPLIANT/P:HF COMPLIANT		TYPE SERIAL NUMBER		PRODUCT TYPE	
												FINISHED PRODUCT	



PART NO.	CONTACT PLATING	DURABILITY
APCI0371-P009A	30u" MIN. GOLD	60 CYCLES.
APCI0371-P007A	15u" MIN. GOLD	25 CYCLES.
APCI0371-P011A	10u" MIN. GOLD	25 CYCLES.
APCI0371-P005A	1~3u" MIN. GOLD	25 CYCLES.

GENERAL TOLERANCES UNLESS SPECIFIED		PART NO.	<b>LOTES</b>			
X ± 0.35	X.° ± 3°	SEE TABLE				
.XX ± 0.25	.X.° ± 2°	APPROVED BY	TITLE 0.5Pitch 67pin M.2 H3.2(KEY M)			
.XXX ± 0.15	.XX.° ± 1°	Frank				
CUSTOMER DRAWING		CHECKED BY	DWG NO. AP-APCI0371			
		Vito				
SIZE	UNITS	DRAWN BY	SHEET   SCALE   REV			
A4	MM [INCH]	Terry				





Assembly stack height schematic diagram

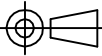
Stack-up Top Mount Double Sided Module for 1.20 max top-side component height

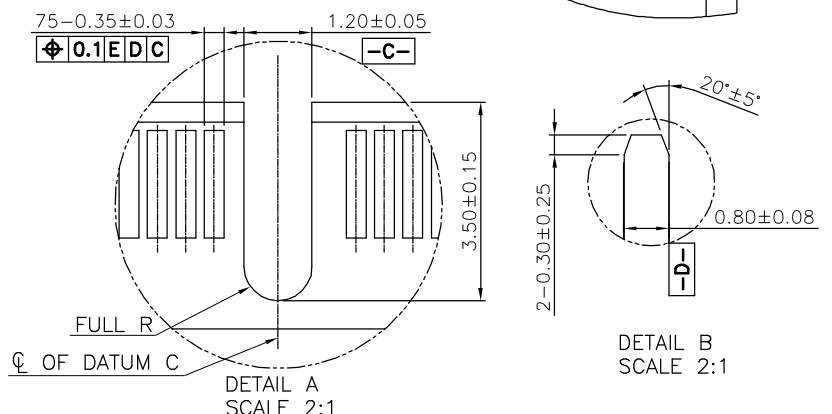
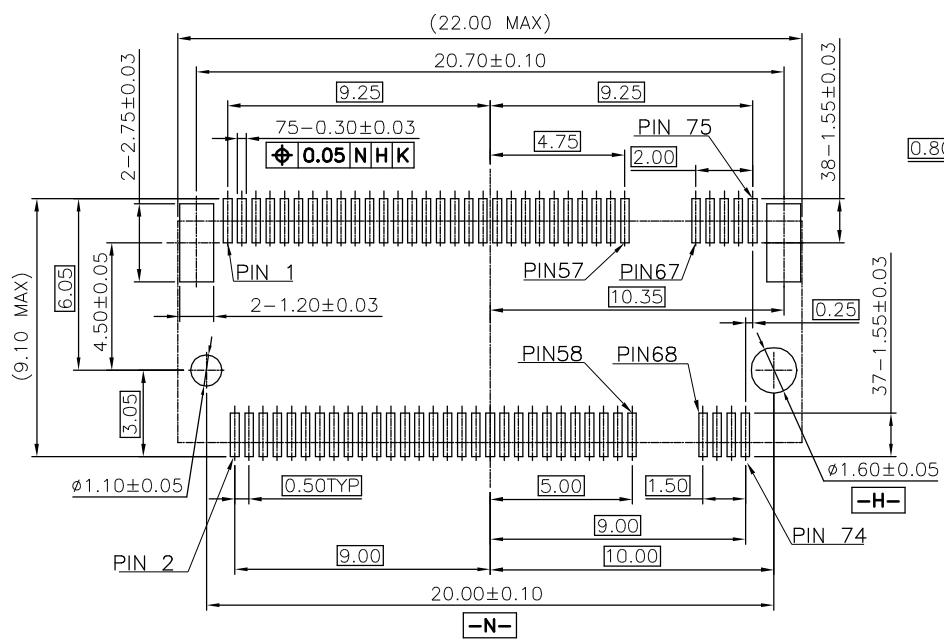
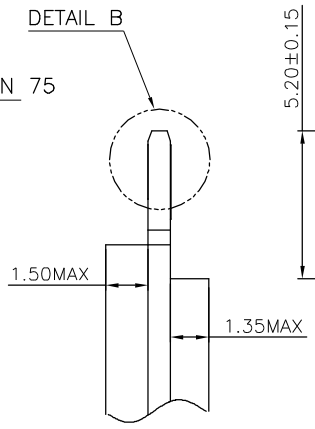
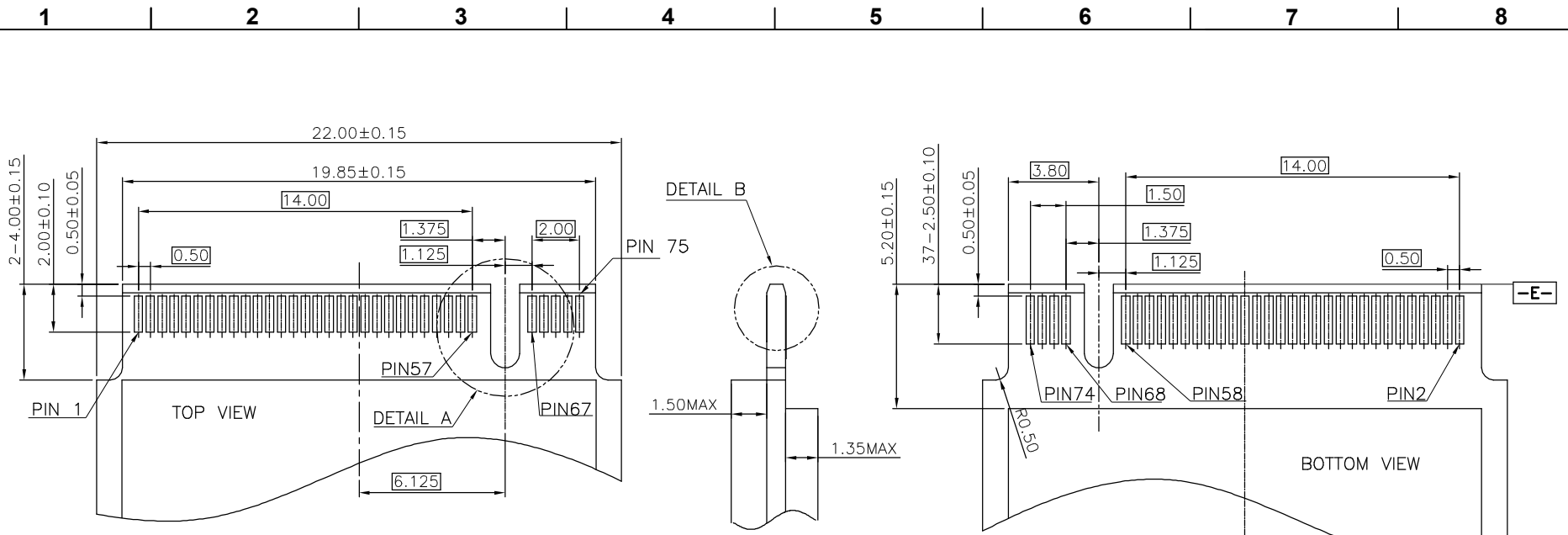
Stack-up Top Mount Double Sided Module for 1.35 max top-side component height

Stack-up Top Mount Double Sided Module for 1.5 max top-side component height

HOW TO MATE AND EXTRACT  
 -----  
 MATE: ① → ④  
 EXTRACT: ⑤ → ⑥

NOTE: Mating force will generate from step 3 to step 4.

GENERAL TOLERANCES UNLESS SPECIFIED		PART NO. SEE TABLE	<b>LOTES</b>			
X ± 0.35	X.° ± 3°					
.XX ± 0.25	.X.° ± 2°	APPROVED BY Frank	TITLE 0.5Pitch 67pin M.2 H3.2(KEY M)			
.XXX ± 0.15	.XX.° ± 1°	CHECKED BY Vito	DWG NO. AP-APCI0371			
CUSTOMER DRAWING		DRAWN BY Terry		SHEET	SCALE	REV
SIZE A4	UNITS MM [INCH]			2 / 4	4 : 1	A1



RECOMMENDED PCB LAYOUT(TOP VIEW)  
DEFAULT TOLERANCE ±0.05MM

GENERAL TOLERANCES UNLESS SPECIFIED		PART NO. SEE TABLE	<b>LOTES</b>			
X ± 0.35	X° ± 3°					
XX ± 0.25	X° ± 2°	APPROVED BY Frank	TITLE 0.5Pitch 67pin M.2 H3.2(KEY M)			
XXX ± 0.15	.XX° ± 1°	CHECKED BY Vito	DWG NO. AP-APCI0371			
SIZE A4	UNITS MM [INCH]	DRAWN BY Terry		SHEET 3 / 4	SCALE 4 : 1	REV A1

